


FORM PTO-1449 (Rev. 2-32)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. 01-53	Serial No. 10/006,610
		Applicant: Jayson Trinh et al.	
		Filing Date: December 3, 2001	Group: 2819

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
 (Use several sheets if necessary)



U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
QG	5 6 0 4 7 7 5	02/18/97	Saitoh et al.	375	376	09/29/95
QG	6 1 2 2 3 3 6	09/19/00	Anderson	375	371	09/11/97


FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

QG	PCT Publication No. WO 01/69837 A2, published September 20, 2001, Clock Data Recovery Circuit Associated with Programmable Logic Device Circuitry

EXAMINER 	DATE CONSIDERED 02/27/05
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